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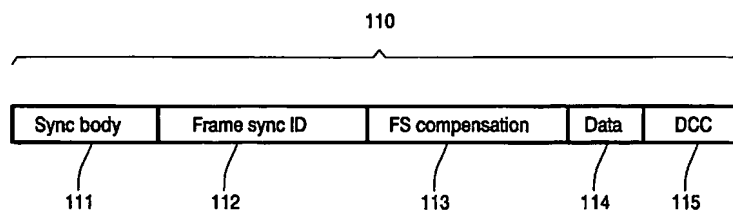
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[Continued on next page]

(54) Title: **BALANCED DISPARITY FRAME SYNC**



Sync body	Frame sync ID	FS compensation
X01010000000010000000010	000 001 →	\$EB
	010 010 →	\$BA
	101 000 →	\$BF
	100 001 →	\$DA
	000 100 →	\$3A
	001 001 →	\$BE
	010 000 →	\$B5

(57) Abstract: A coder is fed with pre-coded data such that the absolute value of the RDS of the code words as produced by the coder is limited. This is achieved by ensuring that in a group of 2 code words the RDS of the first code word is compensated by the RDS of the second code word. The RDS at the end of the second code word is then zero and the excursions of the RDS from the start of the first code word until the end of the second code word are limited because there are only a limited number of bits that can contribute to an increase of the absolute value of the RDS. This principle can easily be applied to the 17PP coder.

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